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Substitute for form 1449/PTO)	Complete	if Known
		Application Number	10/681,244		
INFORMATION DISCLOSURE				Filing Date	October 9, 2003
STATEMENT BY APPLICANT				First Named Inventor	Howard A. Baumer
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(Examiner Name	Gandhi, Dipakkumar B.
Sheet	1	of	2	Attorney Docket Number	1875.4430000

			U.S. PATENT DO	CUMENTS		
Examiner Cite		Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines,	
Initials*	No.1	Number-Kind Code ^{2 (If Known)}	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
	US1	4,627,070	12-02-1986	Champlin et al.		
	US2	4,833,605	05-23-1989	Terada et al.		
	US3	4,833,695	05-23-1989	Greub		
	US4	5,793,990	08-11-1998	Jirgal et al.		
-	US5	5,933,021	08-03-1999	Mohd		
	US6	6,259,693	07-10-2001	Ganmukhi et al.		
	US7	6,137,734	12-24-2000	Schoner et al.		
	US8	6,317,804 B1	11-13-2001	Levy et al.		
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	US10	7,032,139 B1	04-18-2006	Iryami et al.		
	US11	7,035,228 B2	04-25-2006	Baumer		
_	US12	2001/0015664 A1	08-23-2001	Taniguchi		
	US13	2001/0021953 A1	09-13-2001	Nakashima		
	US14	2001/0017595 A1	08-30-2001	Cliff et al.		
	US15	2003/0120791 A1	06-26-2003	Weber et al.		
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	US19	2004/0141531 A1	07-22-2004	Amirichimeh et al.		
	US20	2004/0117698 A1	06-17-2004	Tran et al.		

		Fo	REIGN PATENT DO	CUMENTS		
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where	
		Country Code ³ Number ⁴ Kind Code ³ (if known)			Relevant Passages or Relevant Figures Appear	T ⁶
	FP1					
	FP2					
	FP3					
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	FP5					
	FP6					
	FP7			-1-1		
	FP8					
	FP9					

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Substitute t	for form	1449/PT)	Complete	if Known
				Application Number	10/681,244
INFOR	INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Filing Date	October 9, 2003
STATE				First Named Inventor	Howard A. Baumer
_				Art Unit	2117
	,	•		Examiner Name	Gandhi, Dipakkumar B.
Sheet	2	of	2	Attorney Docket Number	1875.4430000

			U.S. PATENT DO	CUMENTS	
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	No.1	Number-Kind Code ^{2 (If Known)}	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear
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	US22	2006/0250985 A1	11-09-2006	Baumer	
	US23				
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	US40				

			FOREIGN PATENT DO	CUMENTS		
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	FP10					
	FP11					┖
	FP12					
	FP13					
	FP14					
	FP15					Ь_
	FP16					<u> </u>
	FP17					
	FP18					

	Examiner Signature	Date Considered	
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	(Use as man			Art Unit	2117	
				Examiner Name	Gandhi, Dipakkumar B.	
Sheet	1	of	3	Attorney Docket Number	1875.4430000	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
	NPL1	Copy of International Search Report Issued Dec. 14, 2004 for International Application No. PCT/US03/34234, 5 pages.	
	NPL2	Alaska Quad-Port Gigabit Ethernet Over Copper Transceivers, from http://www.marvell.com/products/transceivers/quadport/, 1 page, printed May 8, 2007.	
	NPL3	Alaska X10 Gigabit Ethernet Alaska Gigabit Ethernet Fast Ethernet Physical Layer (PHY) Transceiver Families Provide a Full Range of Ethernet Transceiver Solutions for the Broadband Communications Industry, from http://www.nuhorizons.com/FeaturedProducts/Volume3/Marvell/ phy_transceiver.asp, 4 pages, Copyright 2007, printed May 8, 2007.	
	NPL4	Cisco Search: Results for "cdl" within "News@Cisco", from http://www.cisco.com/pcgibin/search/search.pl, Copyright 1992-2003, printed July 29, 2003.	
	NPL5	Industry Breakthrough: Marvell Announces the First Quad-Port Transceiver to Support Both Copper and Fiber-Optic Gigabit Ethernet Interfaces, from Business Wire at www.encyclopedia.com/printable/aspx?id=1G1:68912211, 4 pages, January 9, 2001, printed May 8, 2007.	
	NPL6	David Maliniak (ed.), Bel's integrated connector modules support Marvell's Alaska quad Gigabit Ethernet transceiver, from http://www.electronicsweb.com/Content/news/, 1 page, December 14, 2000, printed May 8, 2007.	
	NPL7	Marvell Gets Small, from http://www.lightreading.com/document.asp?doc_id=12004&print=true, 1 page, February 19, 2002, printed May 8, 2007.	
	NPL8	Marvell Introduces the Industry's Smallest Quad-Port Gigabit Transceiver Device, Enabling Ultra High Port Density Enterprise Switching Systems, from http://www.marvell.com/press/press NewsDisplay.do?releaseID-41, 3 pages, February 19, 2002, printed May 8, 2007.	
	NPL9	Ed Turner, David Law, Sep. 17-19, 2001, "IEEE P802.3ae MDC/MDIO", http://www.leee802.org/3/efm/public/sep01/turner.sub 1.sub0901.pdf(copy attached).	
	NPL10	Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, from http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html, 3 pages, Copyright 1992-2003, printed July 29, 2003.	

Examiner	Date	
Signature	Considered	

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STATEMENT BY APPLICANT	First Named Inventor	Howard A. Baumer		
(Use as many sheets as necessary)	Art Unit	2117		
	Examiner Name	Gandhi, Dipakkumar B.		
Sheet 2 of 3	Attorney Docket Number	1875.4430000		

		NON PATENT LITERATURE DOCUMENTS	·
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume number, publisher, city and/or country where published	T ²
	NPL11	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Two, pp. 9-55, Copyright 2002.	
	NPL12	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Four, pp. 9-152, Copyright 2005.	
	NPL13	Cisco Systems, Hiroshi Suzuki: Taking Ethernet Beyond the LAN, http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_profile.html	
	NPL14	Cisco Systems, Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html	
	NPL15	Cisco Systems, Innovators Profiles, http://newsroom.cisco.com/dlls/innovators/inn_profiles.html	
	NPL16	Cisco Systems, Optical, http://newsroom.cisco.com/dlls/innovatros/optical/	
-	NPL17	Cisco Systems, Cisco Systems ad Aretae Interactive Launch Internet Powered Property (IP2), http://newroom.cisco.com/dlls/global/asiapac/news/2000/pr_07-25.html.	
	NPL18	"Broadcom Announces the First 0.13-micron CMOS Multi-Rate Gigabit Octal Serializers/Deserializers" [online], Broadcom Press Release, October 29, 2002 [retrieved on 2003-07-16]. Retrieved from the Internet: <url: cgi-bin="" http:="" pr="" prps.cgi?pr_id="PR021029" www.broadcom.com=""> (5 pages).</url:>	
	NPL19	BCM8040 8-Channel Multi-rate 1.0-3.2-GBPS Retimer Switch [online], Broadcom Products Site Guide [retrieved on 2003-07-16]. Retrieved from the Internet: <url: 8040.html="" http:="" products="" www.broadcom.com=""> (2 pages).</url:>	
	NPL20	8-Channel Multi-Rate CMOS Retimer with Full Redundancy [online], BCM8040 Product Brief, Broadcom Corporation (2002) [retrieved on 2003-07-16]. Retrieved from the Internet: <url: 8040.html="" http:="" products="" www.broadcom.com=""> (2 pages).</url:>	

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Signature	Considered	

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¹ Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete. including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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	NPL21	BCM8020 8-Channel Multi-rate 1.0-3.2-GBPS Transceiver [online], Broadcom Products Site Guide [retrieved on 2003-07-16]. Retrieved from the Internet: <url: 8020.html="" http:="" products="" www.broadcom.com=""> (2 pages).</url:>	
=	NPL22	8-Channel Multi-Rate CMOS Transceiver with Full Redundancy [online], BCM8020 Product Brief, Broadcom Corporation (2002) [retrieved on 2003-07-16]. Retrieved from the Internet: <url: 8020.html="" http:="" products="" www.broadcom.com=""> (2 pages).</url:>	
	NPL23	BCM8021 4-Channel Multi-rate 1.0-3.2-GBPS Transceiver with High-Speed Redundancy [online], Broadcom Products Site Guide [retrieved on 2003-07-16]. Retrieved from the Internet: <url: 8021.html="" http:="" products="" www.broadcom.com=""> (2 pages).</url:>	
	NPL24	4-Channel Multirate 1.0-3.2-GBPS Transceiver with High-Speed Redundancy [online], BCM8021 Product Brief, Broadcom Corporation (2002) [retrieved on 2003-07-16]. Retrieved from the Internet: <url: 8021.html="" http:="" products="" www.broadcom.com=""> (2 pages).</url:>	
	NPL25	Broadcom Announces the Industry's First 0.13-micron CMOS Advanced Transciver Solution with Signal Conditioning for Low-Cost 10-Gigabit Over Copper Links [online], Broadcom Press Release, April 29, 2003 [retrieved on 2003-07-16]. Retrieved from the Internet: <url: cgi-bin="" http:="" pr="" prps.cgi?pr_id="PR030429A" www.broadcom.com=""> (4 pages).</url:>	
	NPL26		Í
	NPL27		
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	NPL30		

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